

EXL10 OCS Datasheetfor

HE-EXV1E0, HE-EXV1E2, HE- EXV1E3, HE- EXV1E4, HE- EXV1E5, HE-EXV1E6
HEXT505C100, HEXT505C112, HEXT505C113, HEXT505C114, HEXT505C115, HEXT505C116

1. Specifications

			General Spe	cifications						Control & Logic	Specificatio	ns	
	uired Powei eady state)	-		650n	nA @ 24VDC			Control Language Support			Advanced LadderLogic Full IEC 1131-3 Languages		
Req	uired Powe			25A for <1	ms @ 24VD	С		Logic Program Size			1	1MB, maxir	num
	(Inrush)			DC	Switched			& Logic ScanRate 0.013				0.013mS	,
Primar	y Power Rar	nge		10	0-30VDC			On	Online Programming Changes Supported in Advanced La				nced Ladder
Relat	ive Humidit	У			Non-condens						DigitalInp		2048
Clo	ck Accuracy			,	maximum at		I/OSupport —			DigitalOut		2048	
	,				utes per Moi	nth)			,,		AnalogInp		512
	nding AirTe	mp			°C to+60°C						AnalogOu	ıtputs	512
Sto	rage Temp				°C to +60°C						50,00	00 (words) F	Retentive
	Weight				s (without I/			G	ieneral Purp	ose Registers 16,384 (bits) Rete			
UL/CE			tp://www.heapg.com/content/21-certifications http://www.horner-apg.com/en/support/certification.aspx						4 (bits) Non	-retentive			
		Europe: r	Display Spe					Connectivity					
			Display Spe	anications				Sor	ial Ports			Madular Ia	sck (NAI1 /2)
	splay Type				TFT (550 nit	typical)				1 RS-232 or 1 RS-485 on second Modular Jack (N			r Jack (MJ3)
Resolution					40x480			USB mini-B USB 2.0 (480MHz) Program					
Color					it (65,536)				JSB A	USB 2.0 (480N			
Screen Memory Screen Memory				27 MB					CAN	Remote I/O, Peer-to-Peer Comms, Cscap			
User-Progr	ammable S	creens			1023			2 :	x Ethernet	10/100 Mb (Auto-MDX), Modbus TCP C/S, HTTP, FT SMTP, Cscape, Ethernet IP			S, HTTP, FT
	Backlight			LED – 50,000 hourlife				Ren	note I/O	O SmartRail, SmartStix, SmartBlock, SmartMod			nartMod
Scree	n Update Ra	te		-	Configurable within the scan time.				novabl	MicroSD, support for 32GB max.			
				(perceived as instantaneous in many cases)				e		Application Updates, Datalogging, more			
					Aud	dio	Mic In, Line In, Line Out						
					In	out/Output!							
Model	DC In	DC Out	Relays	HS In	HS Out	mA/V In	mA, RTD/		mA/V Out	Number of Cour	igh-Speed C	Counters	2
Model 2	12	Out	6	4		4	KID,	•	Out	Maximum Frequ		500 k	Hz each
Model 3	12	12		4	2	2				AccumulatorS			its each
Model 4	24	16		4	2	2				710001110101010	ModesSupp		
Model 5	12	12		4	2		2		2	Totalizer			drature
Model 6	12	12		4	2		6*		4*				
		•		•		0 .		of the total DC		PulseMeasurer	nent		quency urement
		d for PWN	re 12-bit Ana I and Pulse Tr a 1 To six mA/V	ain Outputs, 4/17 bit Ana	currently lim log I/O	ited to <65kH					ition Contro OFF Setpoin		

2. Dimensions & Panel Cutout



11.360" (288.5mm)

Cutout tolerance to meet NEMA standards is ±0.005" (0.1mm). Max. Panel Thickness is 5mm.

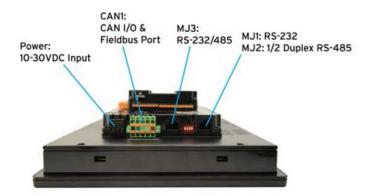
Feb. 1 2016 Page 1 of 10

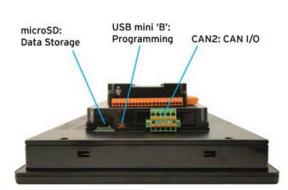


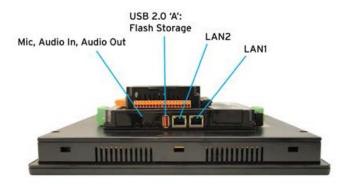
3. Installation Procedures

- 1. Carefully locate an appropriate place to mount the EXL10e. Be sure to leave enough room at the top of the unit for insertion and removal of the microSD card. Also leave enough room at the bottom for the insertion and removal of USB FLASH drives and wiring
- 2. Carefully cut the host panel per the diagram above, creating a $288.5 \text{mm} \times 216 \pm 0.1 \text{mm}$ opening into which the XL7 may be installed. If the opening is too large, water may leak into the enclosure, potentially damaging the OCS. If the opening is too small, the OCS may not fit through the hole without damage.
- 3. Remove all Removable Terminals from the OCS. Insert the OCS through the panel cutout (from the front). The gasket needs to be between the host panel and the OCS.
- 4. Install and tighten the mounting clips (provided in the box) until the gasket forms a tight seal (max torque 7-10 lb-in. [0.8 1.13 Nm])
- 5. Reinstall the I/O Removable Terminal Blocks. Connect communications cables to the serial port, USB ports, Ethernetport, and CAN port as required.

4. Ports & Connectors



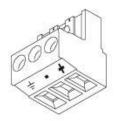






Feb. 1 2016 Page 2 of 10





DC Input / Frame

Torque rating: 4.5 – 7 Lb-In (0.50 – 0.78 N-m)

DC- is internally connected to I/O V-, but is isolated from CAN V-

A Class 2 power supply must be used.

Primary Power Port Pins						
PIN	SIGNAL	DESCRIPTION				
1	Ground	Frame Ground				
2	DC-	Input Power Supply Ground				
3	DC+	Input Power Supply Voltage				



CAN

Locking Spring-Clamp, Two-terminators Per Conductor

Torque rating: 4.5 Lb-In (0.50 N-m)

SHLD and V+ pins are not internally connected to XL7

CAN1 / CAN2 Port Pin						
PIN	SIGNAL	DESCRIPTION	DIRECTION			
1 V-		CAN Ground - Black	-			
2	CN L	CAN Data Low - Blue	IN / OUT			
3	SHLD	Shield Ground - None	(100)			
4	CN H	CAN Data High - White	IN / OUT			
5	V+ (NC)	No Connect - Red	5 2 77 8			



MJ1/2 Independent Serial Ports

MJ1: RS-232 w/Full Handshaking MJ2: RS-485 Half-Duplex

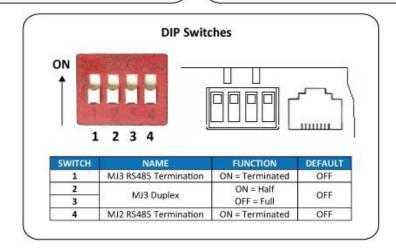
PIN	MJ1	PINS	MJ2 PINS			
	SIGNAL	DIRECTION	SIGNAL	DIRECTION		
8	TXD	OUT				
7	RXD	IN	_	-		
6	0 V	Ground	0 V	Ground		
5	+5V@60mA	OUT	+5V@60mA	OUT		
4	RTS	OUT	1518			
3	CTS	IN	(44)	-		
2	(75)	(-1)	RX-/TX-	IN / OUT		
1	1-3		RX+/TX+	IN/OUT		



MJ3 Serial Port

Two multiplexed Serial Ports on One Modular Jack (8posn)

PIN	MJ3 PINS					
	SIGNAL	DIRECTION				
8	TXD RS232	OUT				
7	RXD RS232	IN				
6	0 V	Ground				
5	+5V@60mA	OUT				
4	TX- RS485	OUT				
3	TX+ RS485	OUT				
2	RX- RS485	IN				
1	RX+ RS485	IN				



Feb. 1 2016 Page 3 of 10



5. Built-in I/O (Model 2, 3, 4, 5 & 6)

All EXL10 models (except the HE-EXV1E0) feature built-in I/O. The I/O is mapped into OCS Register space, in three separate areas – Digital/Analog I/O, High-Speed Counter I/O, and High-speed Output I/O. Digital/Analog I/O location is fixed starting at 1, but the High- speed Counter and High-speed Output references may be mapped to any open register location. For more details on using the High-Speed Counter and High-Speed Outputs, see the XL7 OCS User's Manual (MAN0974-01).

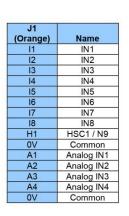
erved P Alarm	1-12 13-32 n/a 1-6	3 1-12 13-31 32 1-12	4 1-24 25-31 32 1-16	5 1-12 13-31 32 1-12	1-12 13-31 32 1-12		
erved P Alarm al Outputs	13-32 n/a	13-31 32	25-31 32	13-31 32	13-31 32		
P Alarm al Outputs	n/a	32	32	32	32		
al Outputs							
	1-6	1-12	1-16	1-12	4.40		
					1-12		
erved	7-24	13-24	17-24	13-24	13-24		
og Inputs	1-4	1-2	1-2	1-2	1-4 ; 33-3		
erved	5-12	3-12	3-12	3-12	n/a		
erved	n/a	1-8	1-8	1-8	1-12		
%AQ1 Analog Outputs n/a n/a n/a 9-10							
%AQ1 Reserved n/a 1-8 1-8 1-8							

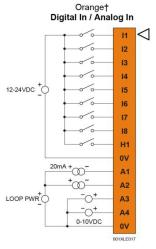
Default Address*					
%11601	Status Bits	1-8			
%Q1601	Command Bits	1-32			
%AI0401	Accumulator 1 & 2	1-8			
%AQ0401	Preload & Match Values	1-12			
*Starting Address locations for %1, %Q, %AI & %AQ may be re-mapped by user					

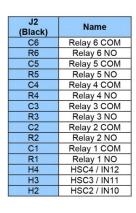
Default Address*							
%11617	1-8						
%Q1** Command Bits 1-2							
n/a n/a n/a							
%AQ421	1-20						
*Starting Address locations for %I & %AQ may be remapped by user							
	**Q1-Q2 are part of the Fixed I/O Map. In High- Speed Output mode they can be used to initiate a Stepper/PTO Move						

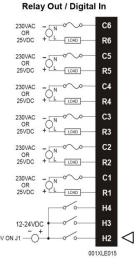
5.1 Model 2 – I/O

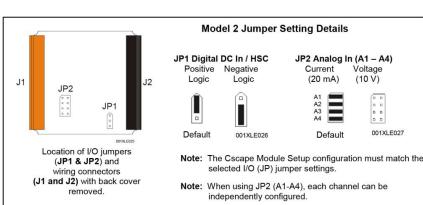
The EXL10 model 2 (HE-EXV1E2) features 12 DC Inputs, 6 Relay outputs, and 4 Analog Inputs. The DC Inputs are 12/24Vdc compatible, and can be jumpered for Positive Logic (sinking), or Negative Logic (sourcing). Two of the inputs (H1-H2) can be used for high-speed functions up to 500kHz. The 12-bit Analog Inputs can be jumpered for voltage (0-10V) or current (4-20mA) on a channel by channel basis. The Relay outputs are isolated, supporting AC and DC voltages, with output currents of up to 3A/relay, 5A total.

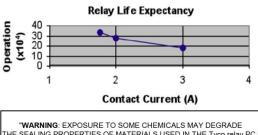












"WARNING: EXPOSURE TO SOME CHEMICALS MAY DEGRADE THE SEALING PROPERTIES OF MATERIALS USED IN THE Tyco relay PC.

Cover / case & base: Mitsubishi engineering Plastics Corp. 5010GN6-30 or 5010GN6-30 M8 (PBT)

Sealing Material: Kishimoto 4616-50K (I part epoxy resin)

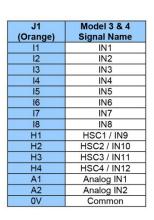
It is recommended to periodically inspect the relay for any degradation of properties and replace if degradation is found

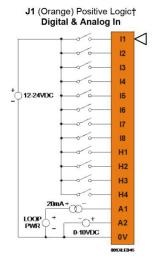
Feb. 1 2016 Page 4 of 10



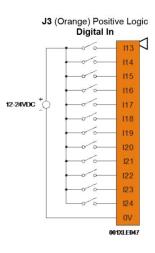
5.2 Model 3 & 4 – I/O

The EXL10 model 3 (HE-EXV1E3) features 12 DC Inputs, 12 DC outputs, and 2 Analog Inputs. The XL7 model 4 (HE-XW1E4) increases the I/O count up to 24 DC Inputs, and 16 DC Outputs and 2 Analog Inputs. The DC Inputs are 12/24Vdc compatible, and can be jumpered for Positive Logic (sinking), or Negative Logic (sourcing). Two of the inputs (H1-H2) can be used for high-speed functions up to 500kHz. The 12-bit Analog Inputs can be jumpered for voltage (0-10V) or current (4-20mA) on a channel by channel basis. The 12/24VDC Outputs feature Electronic Short Circuit protection, and support currents up to 0.5A per point, and 4A total. Two of the DC Outputs can be used for high speed functions (PWM or PTO). The output frequency is limited by the switching capability of the output drivers (about 10kHz), although an optional accessory (HE-XHSQ) can be added to provide parallel output drivers supporting frequencies up to 200kHz.

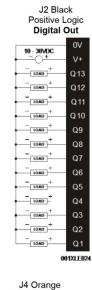


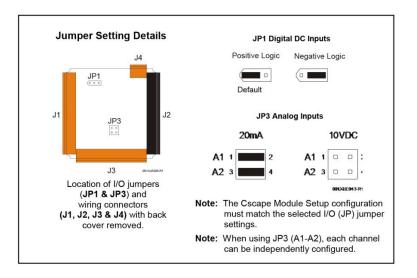


J3 (Orange)	Model 4 only Signal Name
I13	IN13
114	IN14
I15	IN15
I16	IN16
117	IN17
I18	IN18
l19	IN19
120	IN20
I21	IN21
122	IN22
123	IN23
124	IN24
0V	Common

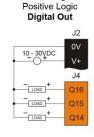


J2 (Black)	Model 3 Name	Model 4 Name				
0V	Common					
V+	V-I	*				
NC	No Connect	OUT13				
Q12	OUT12					
Q11	OUT11					
Q10	OUT10					
Q9	OUT9					
Q8	OUT8					
Q7	OUT7					
Q6	OUT6					
Q5	OUT5					
Q4	OUT4					
Q3	OUT3					
Q2	OUT2 / PWM2					
Q1	OUT1 / PWM1					
*V+ Supp	*V+ Supply for Sourcing Outputs					





J4 (Orange)	Model 4 Name
Q16	OUT16
Q15	OUT15
Q14	OUT14



Note: Model 3 uses J1 & and J2 only.

Model 4 uses J1, J2, J3 & J4.

Feb. 1 2016 Page 5 of 10



5.3 Model 5 – I/O

5.3.1 Hardware Specification

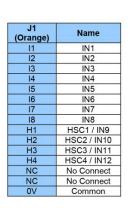
Digital DC Inputs				Digital DC Outputs				
Inputs per Module	12 including 4 co	onfigurable HSC uts		Outputs per Module 12			including 2 configurable PWM outputs	
Commons per Module	•		Commons per Module				1	
Input Voltage Range	12 VDC		Output Type			Sourcing / 10 K Pull-Down		
Absolute Max. Voltage	35 VD		Absolute Max. Voltage			28 VDC Max.		
Input Impedance	10	kΩ		Output Protection	on		Short Circuit	
Input Current	Positive Logic Negative Logic			Max. Output Current per point			0.5 A	
Upper Threshold	0.8 mA	-1.6 mA		Max. Total Current			4 A Continuous	
Lower Threshold	0.3 mA -2.1 mA			Max. Output Supply Voltage			30 VDC	
Max Upper Threshold	8 VDC			imum Output Suppl			10 VDC	
Min Lower Threshold	3 V		Max.	Max. Voltage Drop at Rated Current			0.25 VDC	
OFF to ON Response		ns		Max. Inrush Curr	ent		650 mA per channel	
ON to OFF Response		ms		Min. Load			None	
HSC Max. Switching	10 kHz Totalize	er/Pulse, Edges		OFF to ON Respo			1 ms	
Rate	5 kHz Frequend 2.5 kHz Q			ON to OFF Respo			1 ms	
	2.5 KHZ Q			Output Characteris			Current Sourcing (Positive Logic)	
			og inpu	ts, High Resolutio	n .			
Number of Channels		2		Thermocouple			Temperature Range	
Input Ranges (Selectable)	0 - 10 VDC, 0 – 20 mA, 4 – 20 mA, 100mV PT100 RTD, and J, K, N, T, E, R, S, BThermocoup		ples	E 1652°F t T 752.0°F J 1382.0°I		to 32.0°F (1600°C to 0°C) to -328°F (900°C to -200°C) to -400.0°F (400°C to -240°C) F to -346.0°F (750°C to -210°C) F to -400°F (1370°C to -240°C)		
Safe input voltage range	10 VDC: -0.5 V to +15 V 20 mA: -0.5 V to +6 V RTD / T/C: ±24			Thermocouple Common Mode Range		de	±10V	
Nominal Resolution	10V, 20mA, 100mV: 14 Bits RTD, Thermocouple: 16 Bits			Converter Type			Delta Sigma	
Input Impedance (Clamped @ -0.5 VDC to 12 VDC)	Current Mode: ut Impedance 100 Ω, 35mA Max. Continuous ped @ -0.5 VDC		Max. Error at 25°C (*excluding			*4-20 mA ±0.10%* *0-20 mA ±0.10%* *0-10 VDC ±0.10%* RTD (PT100) ±1.0 °C 0- 100 mV ±0.05%		
			s full	Max Thermocouple Error (After Warm Up Time of One Hour)			±0.2% (±0.3% below-100°C)	
%AI full scale	scale. RTD / T/C: 20 counts / °C		, idii	Conversion Speed, Both Channels Converted		nels	10V, 20mA, 100mV: 30 Times/Second RTD, Thermocouple: 7.5 Times/Second	
Max. Over-Current		35 mA		Conversion Time per Channel		el	10V, 20mA, 100mV: 16.7mS RTD, Thermocouple: 66.7mS	
Open Thermocouple Detect Current		50 nA		RTD Excitation Current		250 μΑ		
	Analog Outputs							
Number of Channels	2			Maximum 20mA load		d	500 Ω	
Output Ranges	0-10 VDC, 0-20 mA			Analog Outputs; Output Points Required		Points	2	
Nominal Resolution		12 Bits		Maximum Error 25° (exclud zero)		uding	0.1%	
Update Rate	Onc	e per scan			tional error for res other than		0.01%	
Minimum 10V load	Minimum 10V load 1 kΩ							

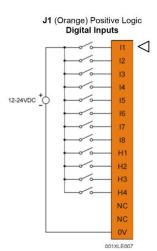
Feb. 1 2016 Page 6 of 10



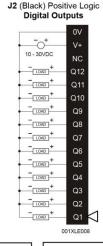
The EXL10 model 5 (HE-EXV1E5) features 12 DC Inputs, 12 DC outputs, with high performance, highly configurable Analog Inputs (2) and Analog Outputs (2). , The DC Inputs are 12/24Vdc compatible, and can be jumpered for Positive Logic (sinking), or Negative Logic (sourcing). Two of the inputs (H1-H2) can be used for high-speed functions up to 500kHz. The 12/24VDC Outputs feature Electronic Short Circuit protection, and support currents up to 0.5A per point, and 4A total. Two of the DC Outputs can be used for high speed functions (PWM or PTO). The output frequency is limited by the switching capability of the output drivers (about 10kHz), although an optional accessory (HE-XHSQ) can be added to provide parallel output drivers supporting frequencies up to 200kHz.

The two high resolution Analog Inputs can be configured for 4-20mA, 0-10V, or 0-100mV at 14-bit resolution. They also can be configured for 16-bit temperature measurement – supporting Thermocouples or RTDs with 0.05°C resolution. The Analog Outputs are sourcing, and can be configured for 4-20mA or 0-10V at 14-bit resolution. Each Analog Input or Output channel can be configured independently for maximum flexibility.

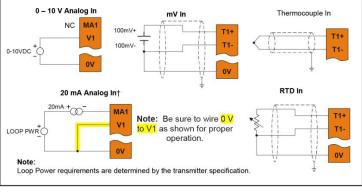


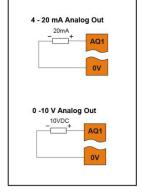


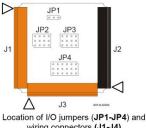
J2 (Black)	Name
0V	Common
V+*	Output Power
NC	No Connect
Q12	OUT12
Q11	OUT11
Q10	OUT10
Q9	OUT9
Q8	OUT8
Q7	OUT7
Q6	OUT6
Q5	OUT5
Q4	OUT4
Q3	OUT3
Q2	OUT2 / PWM2
Q1	OUT1 / PWM1



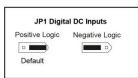
J3 (Orange)	Name			
T1+	Tc (1 +) or RTD (1+) or			
	100mV (1+)			
T1-	Tc (1-) or RTD (1-) or			
	100mV (1-)			
T2+	Tc (2+) or RTD (2+) or			
	100mV (2+)			
T2-	Tc (2-) or RTD (2-) or			
	100mV (2-)			
AQ1	10V or 20mA Out (1)			
AQ2	10V or 20mA Out (2)			
0V	Common			
MA1	0-20mA In (1)			
V1	0-10V In (1)			
0V	Common			
MA2	0-20mA In (2)			
V2	0-10V In (2)			
0V	Common			

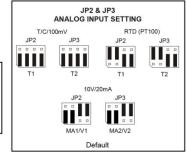






wiring connectors (J1-J4) with back cover removed. **Jumper Setting Details**





ANALOG OUTPUT SETTING CURRENT (20mA) VOLTAGE (10V) AQ2 AQ1 Default

Feb. 1 2016 Page 7 of 10



5.4.1 Hardware Specification

Digital DC Inputs			Digital DC Outputs				
Inputs per Module	12		Outputs per Module	12			
Commons per Module	1		Commons per Module	1			
Input Voltage Range	0 VDC - 24 VDC		Output Type	Half-Bridge			
Absolute Max. Voltage	35 VDC Max.		Absolute Max. Voltage	30 VDC Max.			
Input Impedance	10 kΩ		Output Protection	Short Circuit & Overvoltage			
input impedance	10 K22		Max. Output Current per	0.5 A			
Input Current	Positive Logic	Negative Logic	point	0.5 A			
Minimum 'On' current	0.8 mA	-1.6 mA		2A total current (all drivers) UL-			
Maximum 'Off' current.	0.3 mA	-2.1 mA	Max. Total Current per driver	rated, 6A UL pending			
N: (0.11	0.1/00		(Q1-4, Q5-8, Q9-12).	30 VDC			
Min 'On' Input	8 VDC		Max. Output Supply Voltage				
Max 'Off' Input	3 VDC		Minimum Output Supply Voltage	10 VDC			
OFF to ON Response	1 ms		Max. Voltage Drop at Rated Current	0.25 VDC			
ON to OFF Response	1 ms		Min. Load	None			
Galvanic Isolation	None.		I/O Indication	None			
Logic Polarity	Positive and Ne Common pin lev		Galvanic Isolation	None			
I/O Indication	None.	-	OFF to ON Response	150nS			
High Speed Counter Inputs*	4 - DIN 8-12		ON to OFF Response	150nS			
High Speed Counter Max	XLE/T/6/10 (10)	(Hz max)	PWM Out*	XLE/T/6/10 (65KHz max)			
Freq*	XL4/7 EXL6/10	(500KHz max)	1 VVIVI Out	XL4/7 EXL6/10 (500KHz max)			
Connector Type	3.5mm Pluggab connector	ie cage clamp	Output Characteristics	Current Sourcing (Pos logic)			
Analog Inputs			_				
Number of Channels	6		Absolute max Input Voltage	-0.5 -12V dc. (+/-30Vdc)			
	0–20mA, 4-20 n		Input Impedance	$T/C / RTD / mV > 2 M\Omega$			
Input Range	0-60mV, 0-10V		(Clamped @ -0.5 to	mA: 15 Ω + 1.5 V			
mpat range	T/C - J, K, N, T,		10.23VDC).	V: 1.1 MΩ			
	RTD - PT100, PT1000		,				
Nominal Resolution		riable depending ut type)	Galvanic Isolation	None			
Sensor Range and Accuracy	Input Type	Range		Accuracy			
	TC J	-120 to 10	00°C / -184 to 1832°F	± 0.2% FS ± 1°C			
	TC K	-130 to 13	72°C / -202 to 2501.6°F	± 0.2% FS ± 1°C			
	TC T		0°C / -202 to 752°F	± 0.2% FS ± 1°C			
	TC E		0°C / -202 to 1436°F	± 0.2% FS ± 1°C			
	TC N	-130 to 13	00°C / -202 to 2372°F	± 0.2% FS ± 1°C			
	TC R, S		3°C / 68 to 3214.4°F	± 0.2% FS ± 3°C			
	TC B		20°C / 212 to 3308°F	± 0.2% FS ± 3°C			
	PT100/1000		0°C / -328 to 1562°F	± 0.15% FS			
	0-20mA	0-20mA	<u> </u>	± 0.15% FS			
	0-60mV	0-60mV		± 0.15% FS			
	0-00111V	0-00111V		± 0.15% FS			
Conversion Speed	Minimum all channels converted i		n approx 150mS	_ = 0.10701 0			
Analog Outputs	I will influent all CHa		πι αρφίολ. Τούπιο.				
			Minimum Current load	5000			
Number of Channels	4 0 – 10Vdc.			500Ω None			
Output Ranges	0 – 20mA, 4-20mA dc		Galvanic Isolation				
Nominal Resolution	12 Bits		Conversion Speed	Min all channels once per scan.			
Response Time	One update per	ladder scan.					
Max. Error at 25°C	0-20 mA 0.1%	% of full scale.	Additional Error for				
(excluding zero)		of full scale.	temperatures other than 25°C	20mA 0.0126%/°C.			
	1: 1100 1 1 1101						

*see I/O information below for detail regarding HSC and PWM

Feb. 1 2016 Page 8 of 10



5.4.2 Connection Details



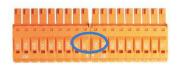




For ease of operability, the high density terminals are divided into more manageable pairs of connectors (J1A + J1B, J2A + J2B, J3A + J3B)

To ensure proper installation, connector symbols must match as seen below:

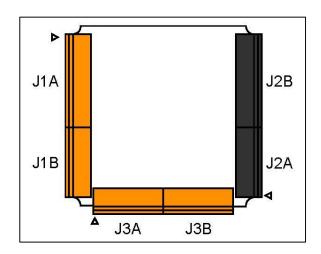




J1 (Orange/ Green)		Signal Name	0 0 11
	l1	V IN1	0 0 13
	12	V IN2	Q + 14
	13	V IN3	15.24VDC
	14	V IN4	÷ 0 0 16
	15	V IN5	√ 0 17
J1A	16	V IN6	0 0 18
	17	V IN7	-0 0 H1
	18	V IN8	- o − H2
	H1	HSC1 / V IN9	-о о нз
	H2	HSC2 / V IN10	□ 0 H4
	H3	HSC3 / V IN11	OV
	H4	HSC4 / V IN12	A1A
	0V	Common	20mA Transmitter
	A1A	Univ. Al 1 pin 1	— A1C
	A1B	Univ. Al 1 pin 2	N/C
	A1C	Univ. Al 1 pin 3	
J1B	N/C	No Connection	T/C (- A2A
	A2A	Univ. Al 2 pin 1	A2B
	A2B	Univ. Al 2 pin 2	— A2C
	A2C	Univ. Al 2 pin 3	N/C
	N/C	No Connection	

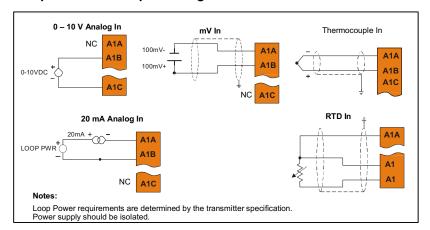
J3 (Orange/ Green)		Signal Name	— N/C — A3A
N/C		No Connection	A3B
	A3A	Univ. Al 3 pin 1	Ø <u>*</u> A3C
	A3B	Univ. Al 3 pin 2	N/C
	A3C	Univ. Al 3 pin 3	A4A
Univ.	N/C	No Connection	A4B
Al	A4A	Univ. Al 4 pin 1	RTD A4C
	A4B	Univ. Al 4 pin 2	
	A4C	Univ. Al 4 pin 3	— N/C
	N/C	No Connection	20mA A5A
	A5A	Univ. Al 5 pin 1	Transmitter 4 A5B
A5B A5C		Univ. Al 5 pin 2	A5C
		Univ. Al 5 pin 3	→ N/C
Univ.	N/C	No Connection	A6A
Al	A6A	Univ. Al 6 pin 1	T/C + A6B
	A6B	Univ. Al 6 pin 2	**************************************
	A6C	Univ. Al 6 pin 3	— A6C
0V		Common	ov
V4		V OUT4*	V4

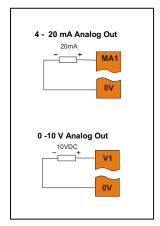
\triangleright	(Black/ Green)	Signal Name	V3			
	V3	V OUT 3*	V2 + LOAD 0-10V Out			
	V2	V OUT 2*	V1 + LOAD - LOAD			
	V1	V OUT 1*	mA4 * LOND 0 20mA Out			
	mA4	mA Out 4*	mA3			
2A	mA3	mA Out 3*	mA2			
	mA2	mA Out 2*	mA1 —			
	mA1	mA Out 1*	Q1			
	Q1	OUT 1 / PWM1	Q2 LOAD			
	Q2	OUT 2 / PWM2	Q3			
	Q3	OUT 3	Q4 LOAD			
	Q4	OUT 4	Q5			
	Q5	OUT 5	Q6			
	Q6	OUT 6	Q7			
op.	Q7	OUT 7	Q8 LOAD			
2B	Q8	OUT 8	Q9			
	Q9	OUT 9	Q10			
	Q10	OUT 10	Q11 LOAD			
	Q11	OUT 11	Q112			
	Q12	OUT 12	V+			
	V+	V External+	•			
	0V	Common	0V			
Note * Both mA & V outputs are active for each output channel, however, only the configured output type is calibrated (maximum 4 channels simultaneously).						





5.4.3 Example Universal Input Wiring Schematic





Configuration

The data registers are as follows:

Digital Inputs	Digital Outputs	Analogue Inputs	Analogue Outputs
%I1-12	%Q1-12	%AI1-4, %AI33-38	%AQ9-12

Note that the first four analogue inputs are mapped to both %Al1-4 and %Al33-36, analogue input channels 5 & 6 are mapped to %Al37 and %Al38 respectively only.

5.4.4 Data values:

The analogue inputs return data types as follows:

Input Mode	Data format	Comment
0-20mA, 4-20mA	0-32000	
0-10V, 0-60mV	0-32000	
T/C, RTD	Temperature in °C or °F to 1 decimal place xxx.y	°C or °F may be selected in the I/O config section. The value is an integer, the user should divide by 10.

5.4.5 Status Register

Register	Description											
%R1	Bit-wise status register enable – R1.1 – R1.9 enable for registers R2 to R9											
%R2	Firmware version											
%R3	Watchdog co	ount – cleared	on po	wer-up.								
%R4	Status bits - 164 3 2 1											
				Reser	ved	Ν	lormal	Config		Calibration		
%R5	Scan rate of the 106 board (average) in units of 100µS.											
%R6	Scan rate of the 106 board (max) in units of 100μS.											
%R7	Channel Status Channel 2 Channel 1											
	8	7	6		5		4	3	2		1	
	Open RTD	Out of	Shorted		Open T/C		Open RTD	Out of			Open T/C	
		Limits	RTD					Limits				
%R8	Channel Sta	tus Chanı	nel 4				Channel 3					
	8	7 6			5		4	3	2		1	
	Open RTD	Out of	Shor	Shorted Open T/C			Open RTD	Out of	Shorted		Open T/C	
		Limits	RTD					Limits	RTD			
%R9	Channel Sta	tus Chanı	nel 6				Channel 5					
	8	7	6		5		4	3	2		1	
	Open RTD	Out of	Shor		Open T/C		Open RTD	Out of	Shorte	d	Open T/C	
		Limits	RTD					Limits	RTD			
%R10-14	Reserved											

Note: For the purposes of the example, the block is shown starting at %R1, but it can be set to anywhere in the %R memory map.

Feb. 1 2016 Page 11 of 11



6. Safety

WARNING: Battery may explode if mistreated. Do not recharge, disassemble or dispose of in fire.

WARNING: EXPLOSION HAZARD – BATTERIES MUST ONLY BE CHANGED IN AN AREA KNOWN TO BE NON-HAZARDOUS

Power input and output (I/O) wiring must be in accordance with Class I, Division 2 wiring methods of the National Electric Code, NFPA 70 for installations in the U.S., or as specified in Section 18-1J2 of the Canadian Electrical Code for installations within Canada and in accordance with the authority having jurisdiction.

This equipment is suitable for use in Class I, Division 2, Groups A, B, C, and D or Non-hazardous locations only.

WARNING: EXPLOSION HAZARD – Do not disconnect equipment unless power has been switched off or the area is known to be non-hazardous.

WARNING: EXPLOSION HAZARD – Substitution of components may impair suitability for Class 1, Division 2.

Digital outputs shall be supplied from the same source as the Operator Control Station

WARNING: Only qualified electrical personnel familiar with the construction and operation of this equipment and the hazards involved should install, adjust, operate, or service this equipment. Read and understand this manual and other applicable manuals in their entirety before proceeding. Failure to observe this precaution could result in severe bodily injury or loss of life.

WARNING: To avoid the risk of electric shock or burns, always connect the earth ground before making any other connections.

WARNING: To reduce the risk of fire, electrical shock, or physical injury it is strongly recommended to fuse all Power Sources connected to the OCS. Be sure to locate fuses as close to the source as possible.

WARNING: Replace fuse with the same type and rating to provide protection against risk of fire and shock hazards.

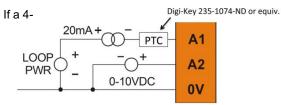
WARNING: In the event of repeated failure, do not replace the fuse again as a repeated failure indicates a defective condition that will not clear by replacing the fuse.

Jumpers on connector JP1 and others shall not be removed or replaced while the circuit is live unless the area is known to be free of ignitable concentrations of flammable gasses or vapors.

7. Common Cause of Analog Input Tranzorb Failure

A common cause of Analog Input Tranzorb Failure on Analog Inputs Model 2, 3, 4 & 5: If a 4-20mA circuit is initially wired with loop power, but without a load, the Analog input could see 24Vdc. This is higher than the rating of the tranzorb. This can be solved by NOT connecting loop power prior to load connection, or by installing a low-cost PTC in series between the load and Analog input.

NOTE†: Refers to Model 2 – orange (pg.5,) Models 3 & 4 – J1 (pg.6) and Model 5 – 20mA Analog In (pg.7.)



8. Technical Support

For assistance and manual updates, contact Technical Support at the following locations:

North America (317) 916-4274 Toll Free: 877-665-5666 http://www.heapg.com

e-mail:techsppt@heapg.com

Europe

(+)353-21-4321-266

http://www.horner-apg.com

e-mail: tech.support@horner-apg.com

Feb. 1 2016 Page 11 of 11